

Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)		Attorney Docket No.: 1207P006D	Application Number: 10/021,744
Page 1 of 5 <i>O P E</i> <i>FEB 11 2002</i> <i>PATENT & TRADEMARK OFFICE</i>		First Named Inventor: Benjamin S. Ting	
		Filing Date: December 5, 2001	

U.S. PATENT DOCUMENTS

Exam. Initial*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (If known)			
		4,020,469		Manning	Technology Center 2100-26-77	
		4,661,901		Veneski	4-28-87	
		4,700,187		Furtek	10-13-87	
		4,720,780		Dolecek	1-19-88	
		4,736,333		Mead et al.	4-5-88	
		4,815,003		Putatunda et al.	3-21-89	
		4,847,612		Kaplinsky	7-11-89	
		4,870,302		Freeman	9-26-89	
		4,918,440		Furtek	4-17-90	
		4,935,734		Austin	6-19-01	
		4,992,680		Benedetti et al.	2-12-91	
		5,144,166		Camarota et al.	9-1-92	
		5,204,556		Shankar	4-20-93	
		5,208,491		Ebeling et al.	5-4-93	

FOREIGN PATENT DOCUMENTS

Exam. Initial*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD- YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ⁵ (If known)				
		EP	0415542 A1	A1	Advanced Micro Devices, Inc.	03/06/91		
		EP	0630115 A2	A2	Pilkington Micro-Electronics Limited	12/21/94		
		GB	2180382 A		Pilkington Micro-Electronics Limited	03/25/87		
		GB	2295738 A		Altera Corporation	06/05/96		

Examiner Signature	<i>[Handwritten Signature]</i>	Date Considered	<i>09/15/05</i>
-----------------------	--------------------------------	-----------------	-----------------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code (WIPO Standard S.3). ⁴For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)		Attorney Docket No.: 1207P006D	Application Number: 10/021,744
Page 2 of 5 <i>TPS</i> <i>FEB 11 2002</i> <i>BESTMAILED FEB 11 2002</i>		First Named Inventor: Benjamin S. Ting	
		Filing Date: December 5, 2001	

U.S. PATENT DOCUMENTS

Exam. Initial*	Cite No.'	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code' (If known)			
		5,221,865		RECEIVED Phillips et al.	6-22-93	
		5,243,238		Kean	9-7-93	
		5,260,610	FEB 15 2002	Pedersen et al.	11-9-93	
		5,296,759	Technology Center 2100	Sutherland et al.	3-22-94	
		5,298,805		Garverick et al.	3-29-94	
		5,329,470		Sample et al.	7-12-94	
		5,396,126		Britton et al.	3-7-95	
		5,406,525		Nicholes	4-11-95	
		5,455,525		Ho et al.	10-3-95	
		5,457,410		Ting	10-10-95	
		5,469,003		Kean	11-21-95	
		5,477,067		Isomura et al.	12-19-95	
		5,485,103		Pederson et al.	1-16-96	
		5,519,629		Snider	5-21-96	

FOREIGN PATENT DOCUMENTS

Exam. Initial*	Cite No.'	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD- YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T*
		Office'	Number'	Kind Code' (If known)				
		PCT	WO92/08286	A1	Concurrent Logic, Inc.	05/14/92		
		PCT WO94	WO94/28475	A1	Regents of the University of California	12/08/94		
		PCT 96	WO96/05964	A1	Minnesota Mining and Manufacturing Company	02/29/96		
		PCT 96	WO96/35261	A1	ETR, Inc.	11/07/96		

Examiner Signature		Date Considered	69/15/2002
-----------------------	--	-----------------	------------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

*Unique citation designation number. *See attached Kinds of U.S. Patent Documents. *Enter Office that issued the document, by the two-letter code (WIPO Standard S.3). *For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. *Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. *Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)		Attorney Docket No.: 1207P006D	Application Number: 10/021,744			
Page 3 of 5 		First Named Inventor: Benjamin S. Ting				
		Filing Date: December 5, 2001				
U.S. PATENT DOCUMENTS						
Exam. Initial*	Cite No.*	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (If known)			
		5,550,782		Cliff et al.	8-27-96	
		5,552,722		Kean	9-3-96	
		5,572,148		Lytle et al.	11-5-96	
		5,581,199		Pierce et al.	12-3-96	
		5,581,767		Katsuki et al.	12-3-96	
		5,835,405		Tsui et al.	11-10-98	
		5,894,228		Reddy et al.	4-13-99	
		5,903,165		Jones et al.	5-11-99	
RECEIVED						
FEB 15 2002						
Technology Center 2100						

FOREIGN PATENT DOCUMENTS								
Exam. Initial*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD- YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ⁵ (If known)				
<i>WFO</i>	<i>PCT</i>	WO94/10754	A1	Xilinx, Inc.	05/11/94			
<i>WFO</i>	<i>PCT</i>	WO95/04404	A1	Advantage Logic, Inc.	02/09/95			
Examiner Signature						Date Considered	<i>09/16/04</i>	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code (WIPO Standard S.3). ⁴For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)		Attorney Docket No.: 1207P006D	Application Number: 10/021,744
<p>Page 4 of 5</p> <p style="text-align: center;">FEB 11 2002 U.S. PATENT AND TRADEMARK OFFICE</p>		First Named Inventor: Benjamin S. Ting	
		Filing Date: December 5, 2001	
OTHER ART - NO PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Translation ²
<i>e</i>		RICHARD CLIFF et al., "A Dual Granularity and Globally Interconnected Architecture for a Programmable Logic Device", Proceedings of the IEEE 1993 Custom Integrated Circuits Conference, May 1993, pp. 7.3.1-7.3.5.	Technology Center 2100 RECEIVED FEB 13 2002
		Altera Corporation, Data Sheet, "Flex EPF81188 12,000-Gate Programmable Logic Device", September 1992, Version 1.	
		MINNICK, R.C., "A Survey of Microcellular Research", Journal of the Association for Computing Machinery, Vol. 14, No. 2, April 1967, pp. 203-241.	
		SHOUP, R.G., "Programmable Cellular Logic Arrays", Ph.D dissertation, Carnegie-Mellon University, Pittsburgh, PA, March 1970 - Partial.	
		SPANDORFER, L.M., "Synthesis of Logic Functions on an Array of Integrated Circuits", UNIVAC, Division of Sperry Rand Corporation, Blue Bell, PA, Contract AF 19 (628)2907, AFCRL 66-298, Project No. 464504, November 30, 1965.	
		PING-TSUNG WANG et al., "A High Performance FPGA with Hierarchical Interconnection Structure", Institute of Electrical and Electronic Engineers, pp. 239-242, May 30, 1994.	
		Motorola Product Brief, "MPA10XX Field Programmable Gate Arrays", pp. 4 pages total, September 27, 1993.	
		KRAMBECK, R.H. "ORCA: A High Performance, Easy to Use SRAM Based Architecture", Wescon '93 record, pp. 310-320, September 20-30, 1993.	
		BUFFOLI et al., "Dynamically Reconfigurable Devices used to Implement a Self-Tuning, High Performance PID Controller", IEEE 1989, pp. 107-112.	
		DEVADAS, S., "Boolean Decomposition of Programmable Logic Arrays", IEEE 1988, pp. 2.5.1-2.5.5.	
<i>sk</i>		VIDAL, J., "Implementing Neural Nets with Programmable Logic", IEEE 1988, pp. 50-53.	

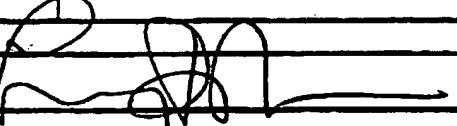
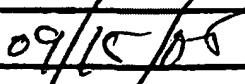
Examiner Signature	Date Considered	09/15/05
--------------------	-----------------	----------

*Examiner: Initial if reference considered whether or not citation is in conformance with MP&P 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)		Attorney Docket No.: 1207P006D	Application Number: 10/021,744
Page 5 of 5 		First Named Inventor: Benjamin S. Ting	
		Filing Date: December 5, 2001	
OTHER ART - NO PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Translation ²	
	YACHYANG SUN et al., "An Area Minimizer for Floorplans with L-Shaped Regions", 1992, Int'l. Conference on Computer Design, pp. 383-386.		
	ATMEL Corporation, "Field Programmable Gate Arrays - AT6000 Series", 1993.		
	DAVE BURSKY, "Fine-Grain FPGA Architecture Uses Four Levels of Configuration Hierarchy", Electronic Design, pp. 33-34, October 1, 1993.		
	DICK L. LIU et al., "Design of Large Embedded CMOS PLA's for Built-In Self-Test", IEEE 1988, pp. 50-53.		
	Xilinx, "The Programmable Gate Array Data Book", 1992		
	F. ZLOTNICK et al., "A High Performance Fine-Grained Approach to SRAM based FPGA's, Wescon '93 Record, pp. 321-326, September 28-30, 1993.		
	BARRY K. BRITTON et al., "Optimized Reconfigurable Cell Array Architecture for High Performance Field Programmable Gate Arrays", Proceedings of the IEEE 1993 Custom Integrated Circuits Conference, May 1993, pp. 7.2.1-7.2.5.		
	TING et al., "A New High Density and Very Low Cost Reprogrammable FPGA Architecture", pp. 1-10. <i>2/18/99</i>		
RECEIVED FEB 15 2002 Technology Center 2100			

Examiner Signature		Date Considered	
--------------------	---	-----------------	---

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.